

Figure 3

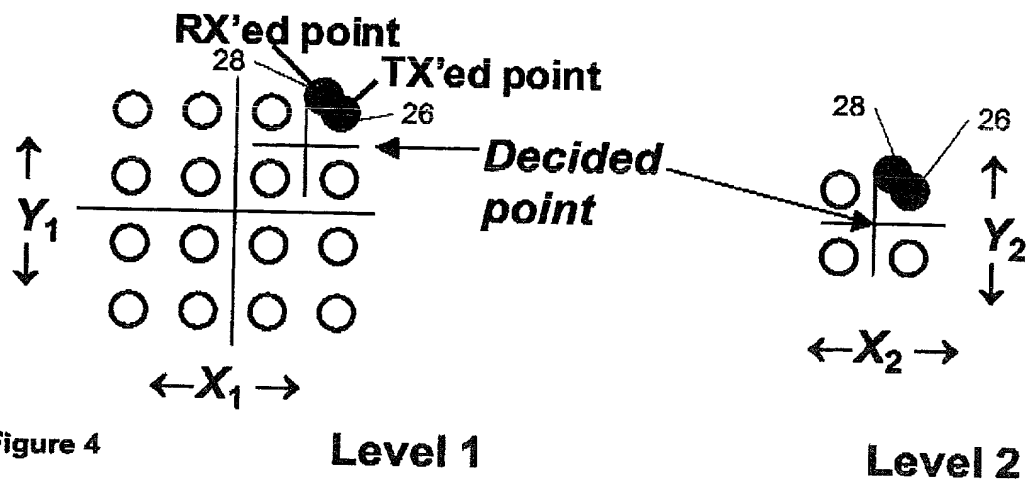


Figure 4

Level 1

Level 2

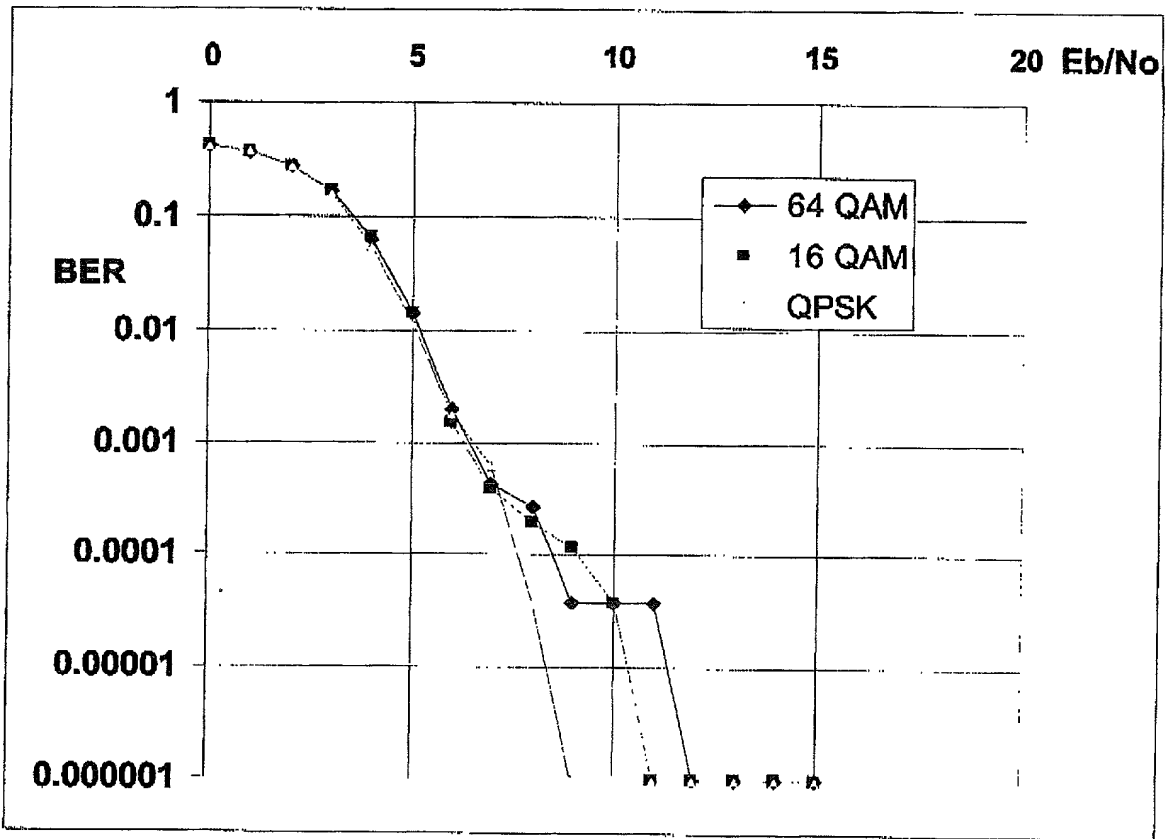


Figure 5

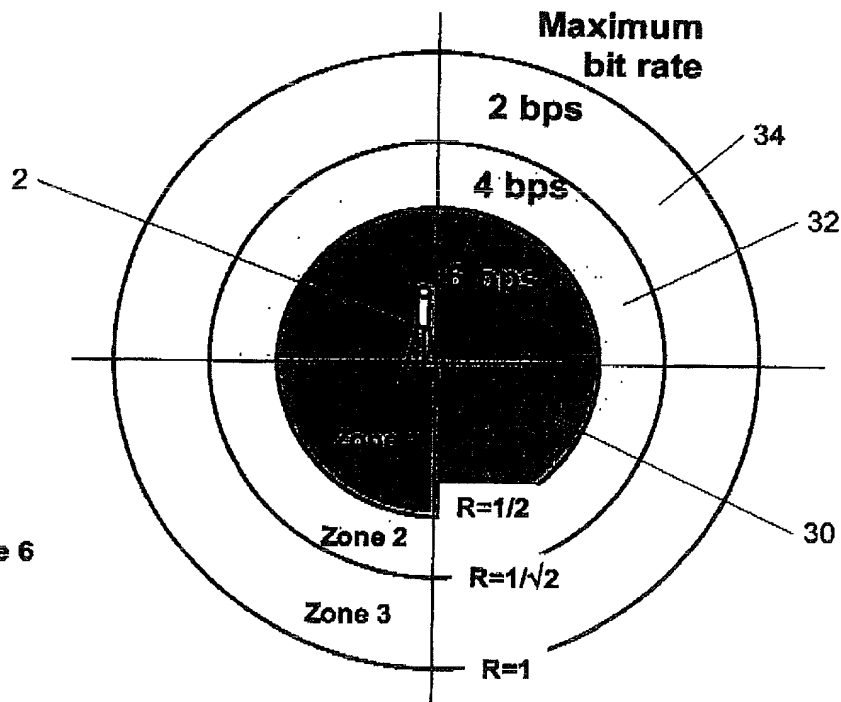


Figure 6

## 64-QAM

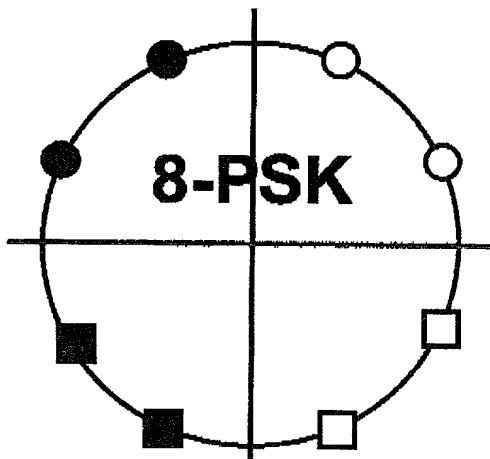


Figure 7

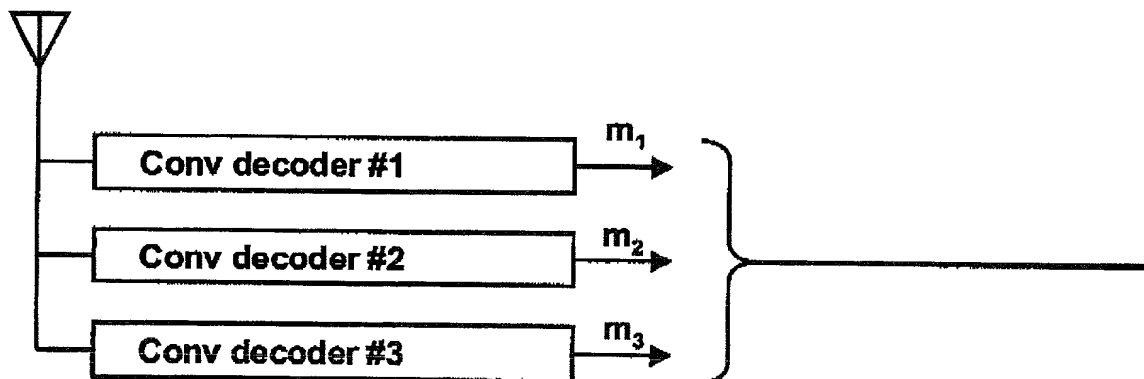
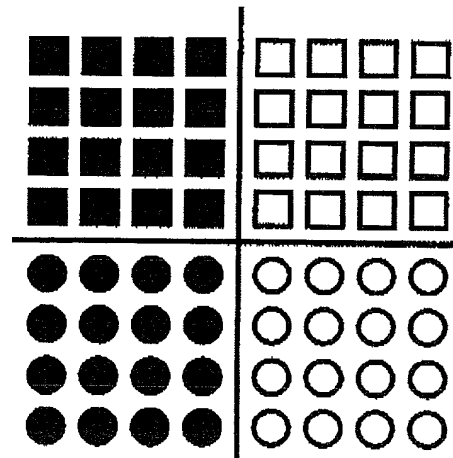


Figure 9

**3-state ARQ indicates which blocks were successfully decoded.**

**If  $m_3$  fails the failed bits are recycled.**

**If  $m_2$  &  $m_3$  fail the failed bits are recycled.**

Split message into segments  $m_1, m_2, m_3$  which have constant length after coding

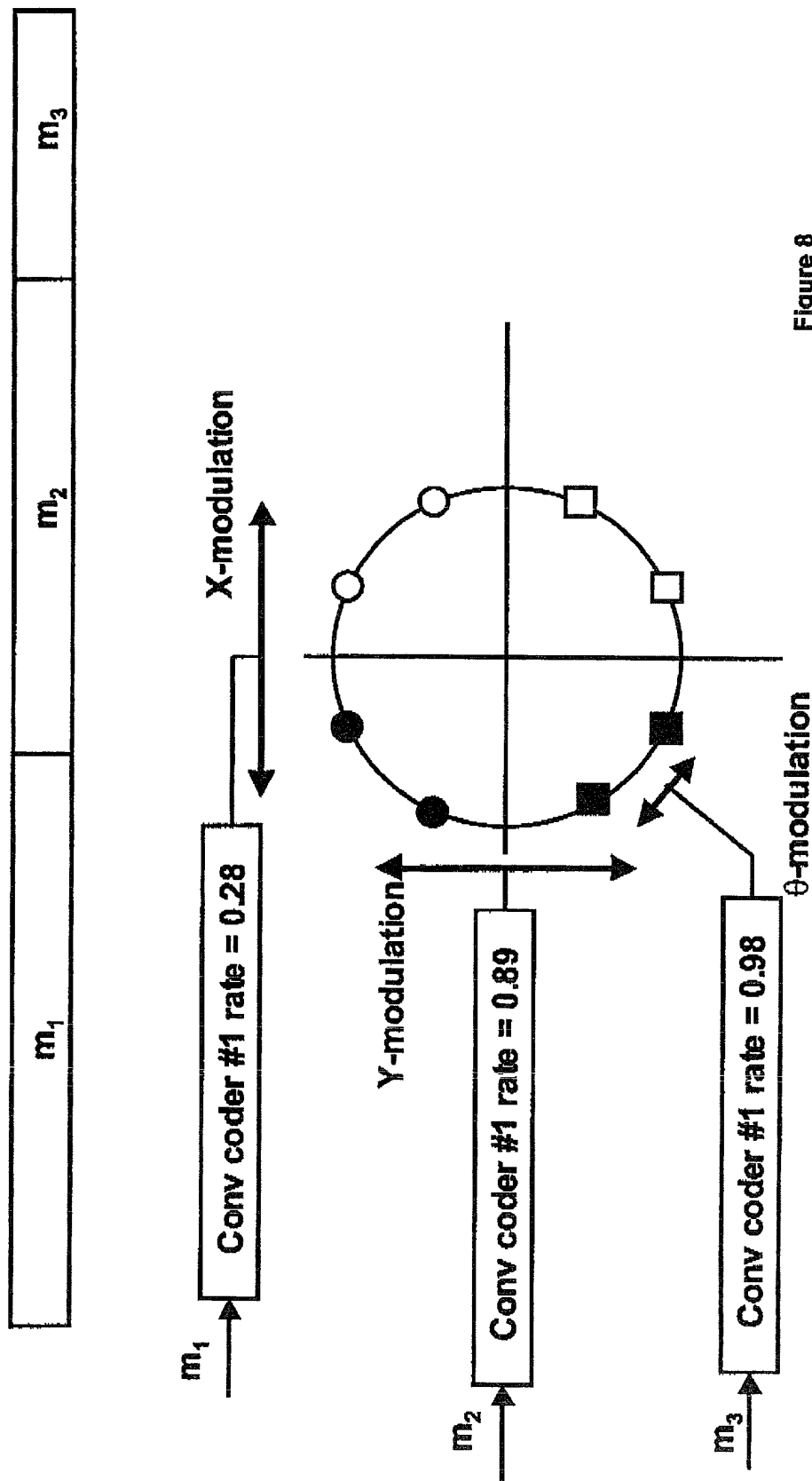


Figure 8

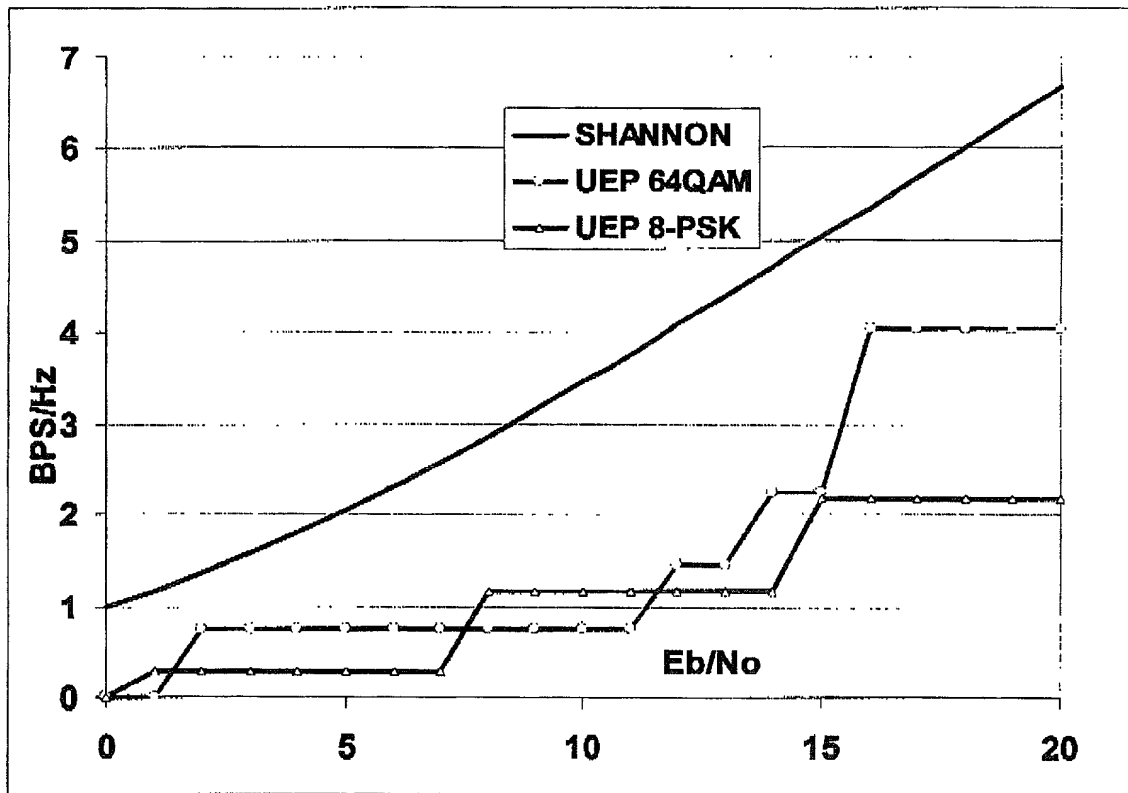


Figure 10